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Demystifying Switched-Capacitor Circuits: Demystifying ...

This chapter begins with a number of low-voltage switched-capacitor (SC) circuit techniques such as clock boosting, bootstrapped switch, and switched operational amplifiers (op-amp). It then explores two accuracy-enhancement techniques suitable to desensitize SC circuits from op-amp imperfections: autozeroing and correlated double sampling.

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Demystifying Switched Capacitor Circuits (v. 1) This publication aids designers to grasp basic concepts and also style concepts by presenting instinctive and also physical descriptions of switched-capacitor circuits. Numerous circuit instances are reviewed and also the author emphasizes the most fundamental and important principles associated with carrying out advanced switched-capacitor circuits for analog signal handling and power administration applications.

Demystifying Switched Capacitor Circuits (v. 1)

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The simplest switched-capacitor (SC) circuit is the switched-capacitor resistor, made of one capacitor C and two switches S_1 and S_2 which connect the capacitor with a given frequency alternately to the input and output of the SC. Each switching cycle transfers a charge $\{ \displaystyle q \}$ from the input to the output at the switching frequency

Switched capacitor - Wikipedia

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The book is full of circuit design examples and real-world stuff on analog/mixed-signal switched-capacitor circuit design. The reference list is plentiful and convenient for one to trace all the excitements in this field. Would be a good addition to any analog designer's library.

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If a diode-and-capacitor combination is placed in parallel to the switch, the peak voltage can be stored in the capacitor, and the capacitor can be used as a DC source with an output voltage greater than the DC voltage driving the circuit. This boost converter acts like a step-up transformer for DC signals.

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