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not making them mandatory for following the main flow of the chapters. Design is emphasized throughout, and switching algebra is developed as a tool for analyzing and implementing digital systems. The presentation includes excellent coverage of minimization of combinational circuits, including multiple

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output ones, using the Karnaugh map and iterated consensus. There are a number of examples of the design of larger systems, both combinational and sequential, using medium scale integrated circuits and programmable logic devices. The third edition features two chapters on sequential systems. The first chapter covers analysis

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of sequential systems and the second covers design. Complete coverage of the analysis and design of synchronous sequential systems adds to the comprehensive nature of the text. The derivation of state tables from word problems further emphasizes the practical implementation of the material being

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presented. The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic, instead of electronics, level. It covers a range of topics, from number system theory to asynchronous logic design. A solution

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software is well integrated into the book. A CD-ROM that contains Altera's Quartus CAD software comes free with every copy of the text. The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs).

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digital logic, this title includes an extensive set of examples well integrated into the body of the text, giving students multiple opportunities to understand the topics being presented.

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used to show how larger circuits are designed. The book emphasizes CAD through the use of Altera's Quartus II CAD software, a state-of-the-art digital circuit design package. This software produces automatic mapping of designs written in VHDL into Field Programmable Gate Arrays).

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courses consisting of an introduction to logic circuits (Chapters 1-7) followed by logic design (Chapters 8-13) or a single, accelerated course that uses the early chapters as reference material.

Description: The book is an attempt to make Digital Logic Design easy and

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simple to understand. The book covers various features of Logic Design using lots of examples and relevant diagrams. The complete text is reviewed for its correctness. This book is an outcome of sincere effort and hard work to bring concepts of Digital Logic Design close to the audience of this book. The salient

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